

A Universal Life-Test System

Stuart Hobday

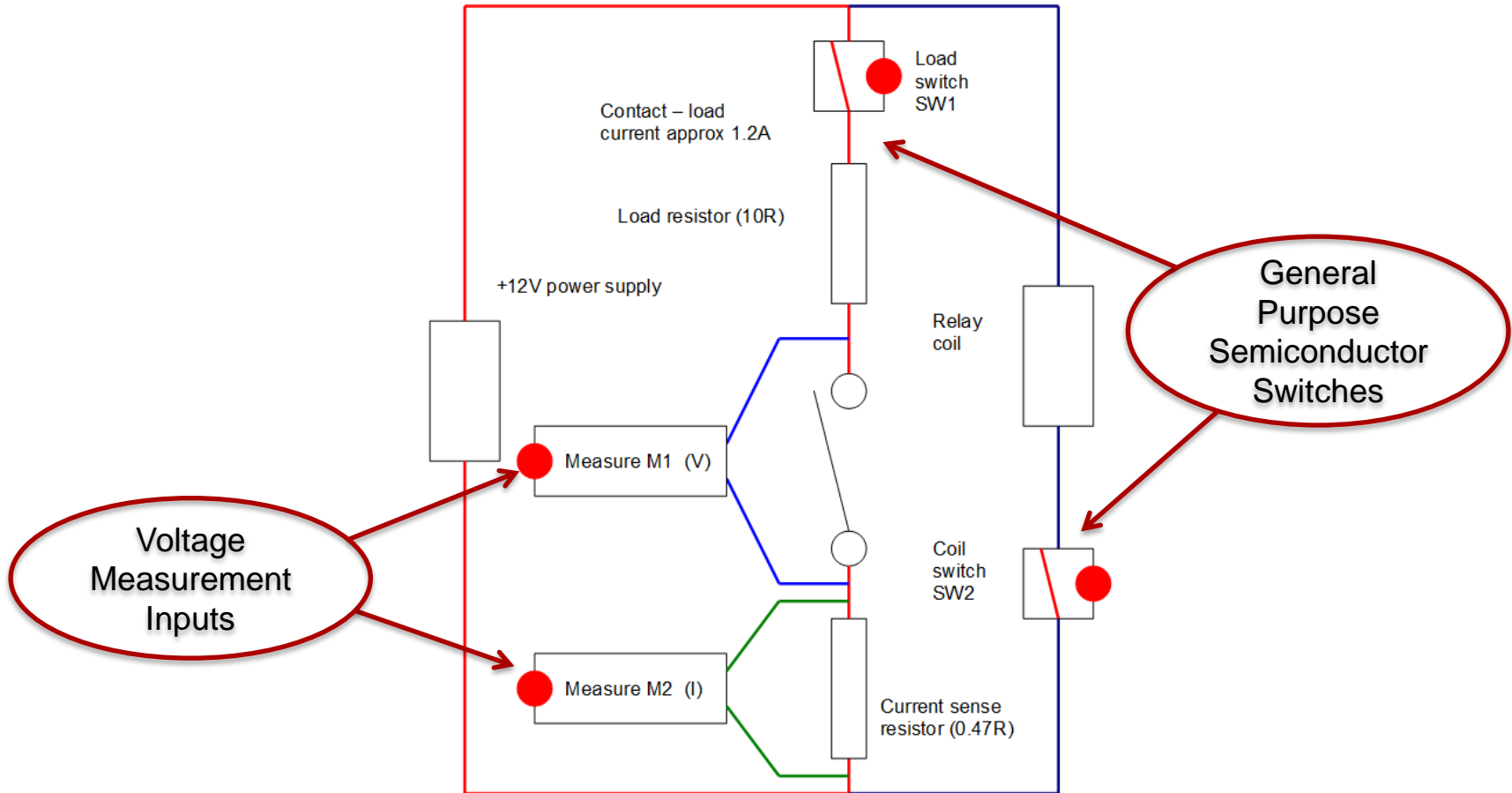
BEng MIET Principal Engineer

Introduction

- Unique insight into life-time of the modern relay
- Trend analysis using real-time oscilloscope style graphics
- Flexible environment that adapts to the life-test requirements

Our Objectives

- Create a flexible system architecture
- Simple to understand and configure
- Minimal hardware requirements
- Monitor contact voltage on every cycle
- Provide additional failure insight through contact timing and operate/release voltage data.



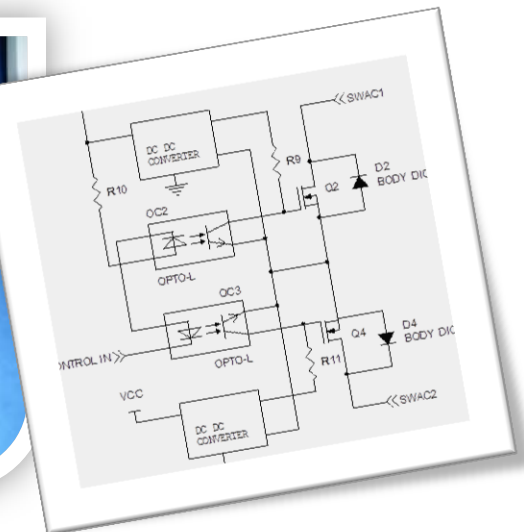
Voltage
Measurement
Inputs

General
Purpose
Semiconductor
Switches

● Denotes a software-controlled resource

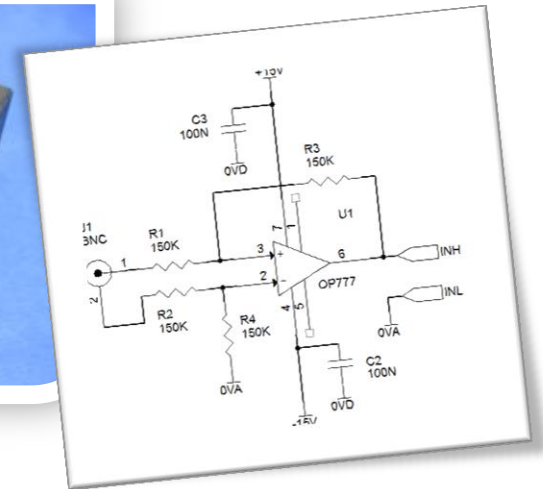
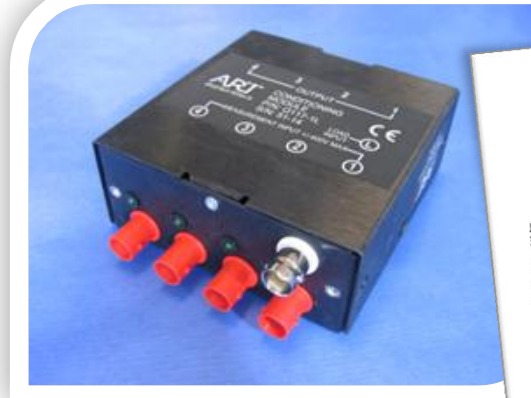
General Purpose Switching

- Solid state switching
- Higher switching rates
- Shorter response times
- Wide range of voltage and current capability
- AC/DC switching



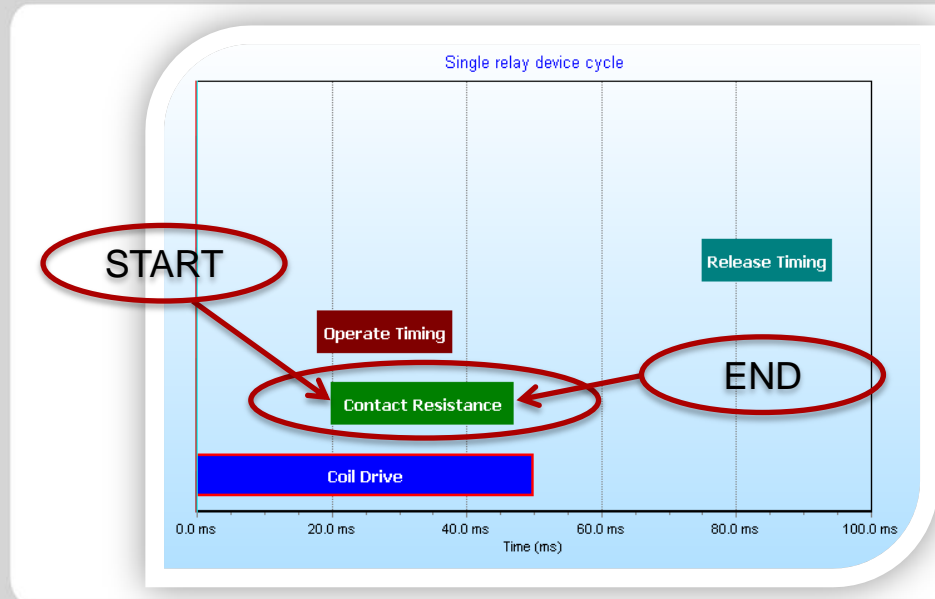
Voltage Measurement Inputs

- Modular design - providing quad differential sense inputs
- Programmable gain/attenuation
- AC & DC measurements

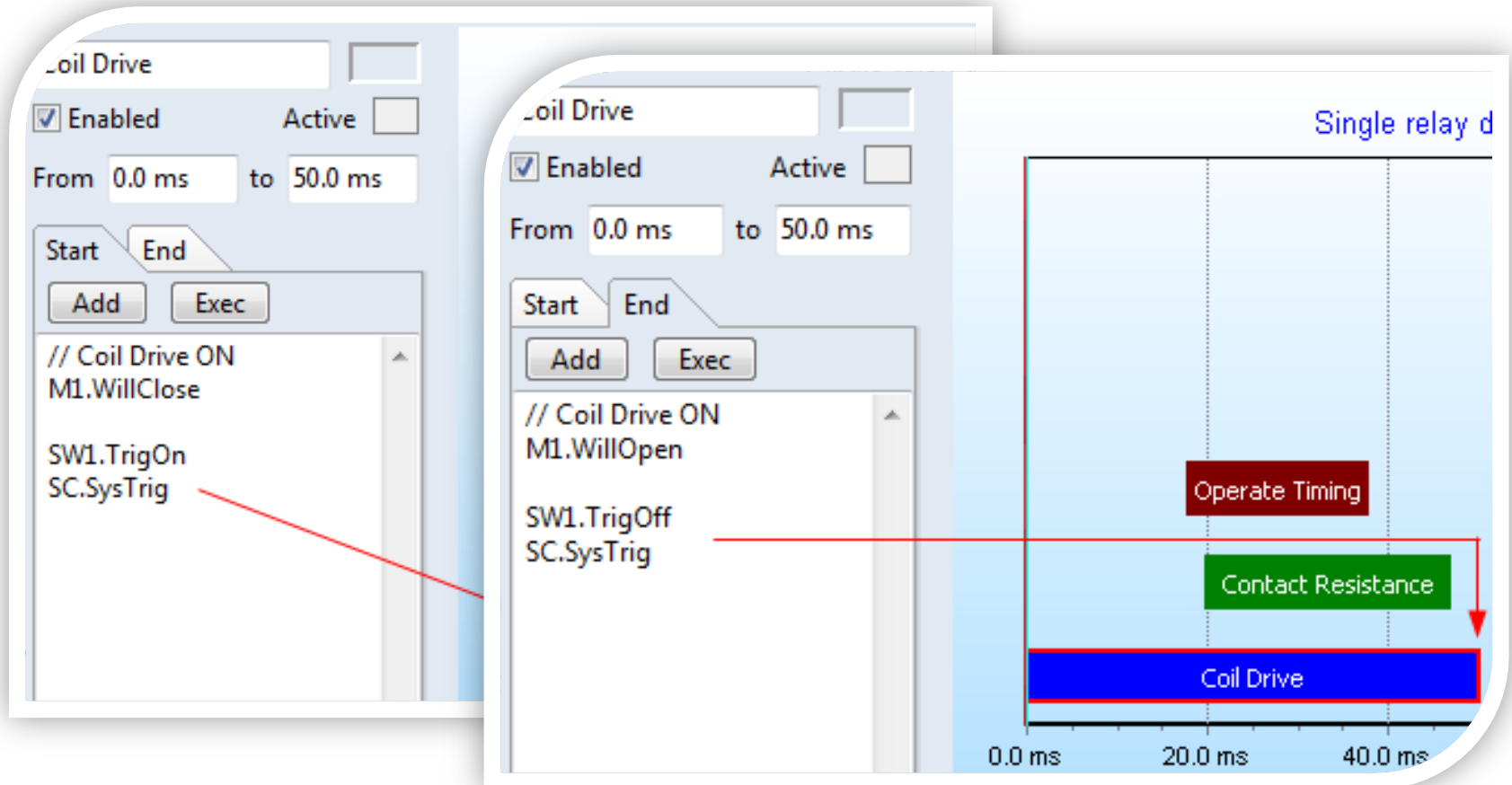


Life-test Programming

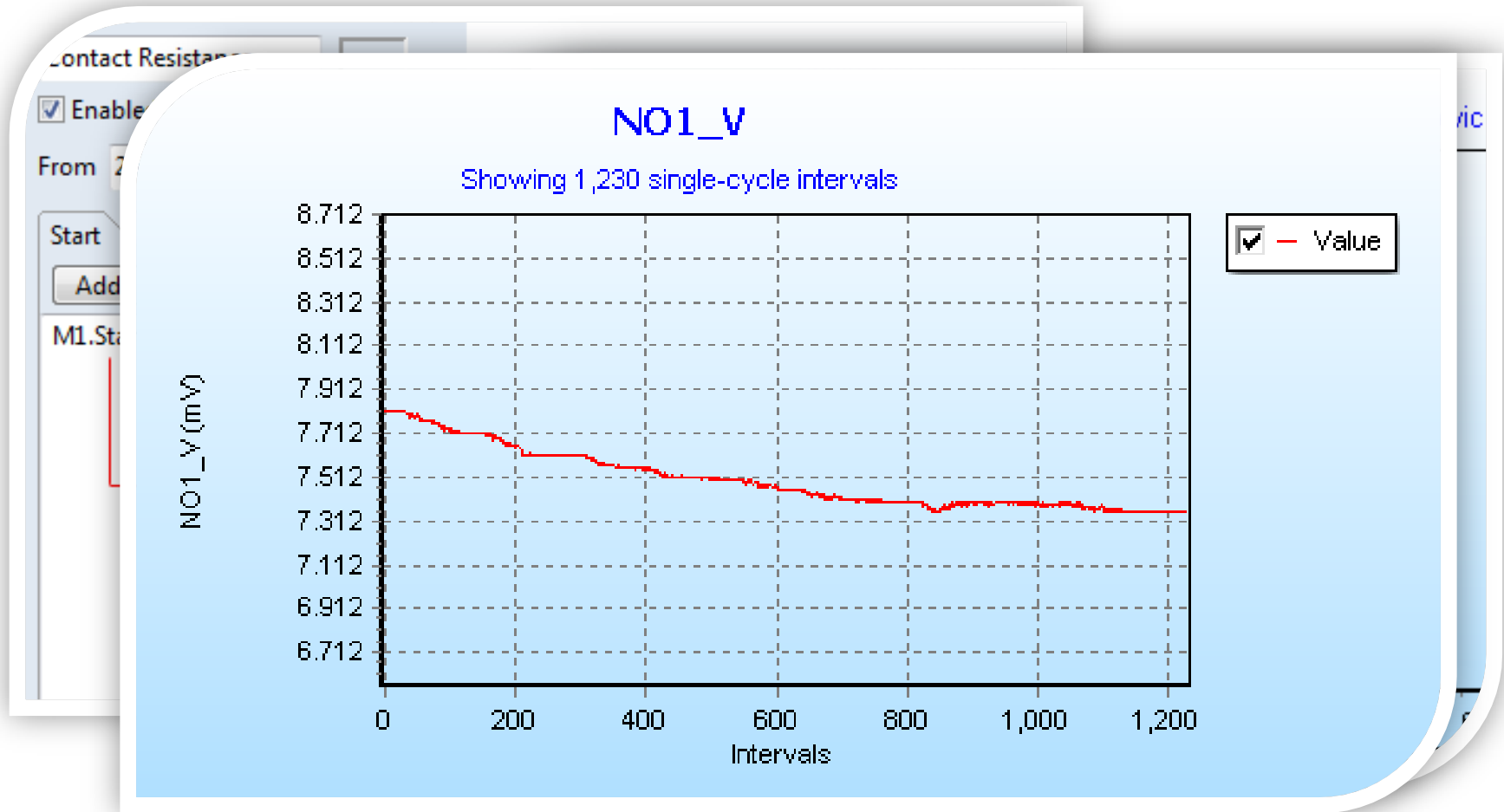
- Based upon a single cycle
- Life-test is a single cycle repeated N times
- Event based hardware execution using time slots



Coil Drive Switching



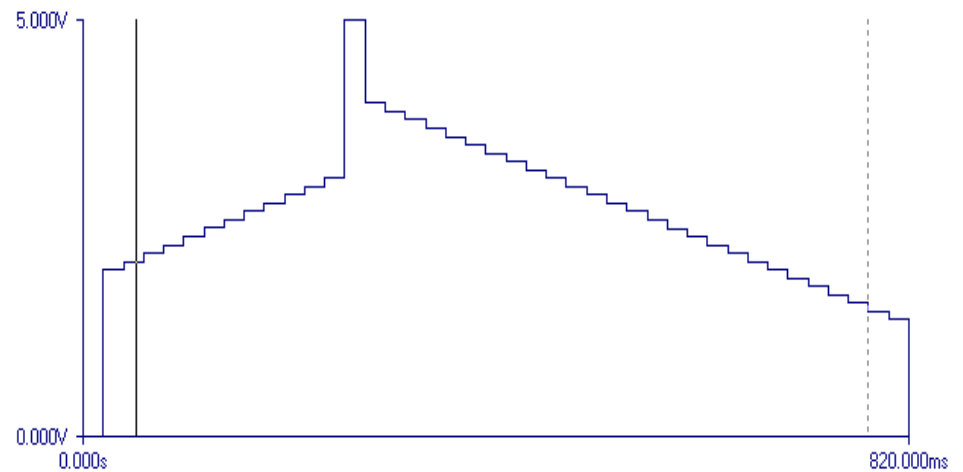
Contact Measurements



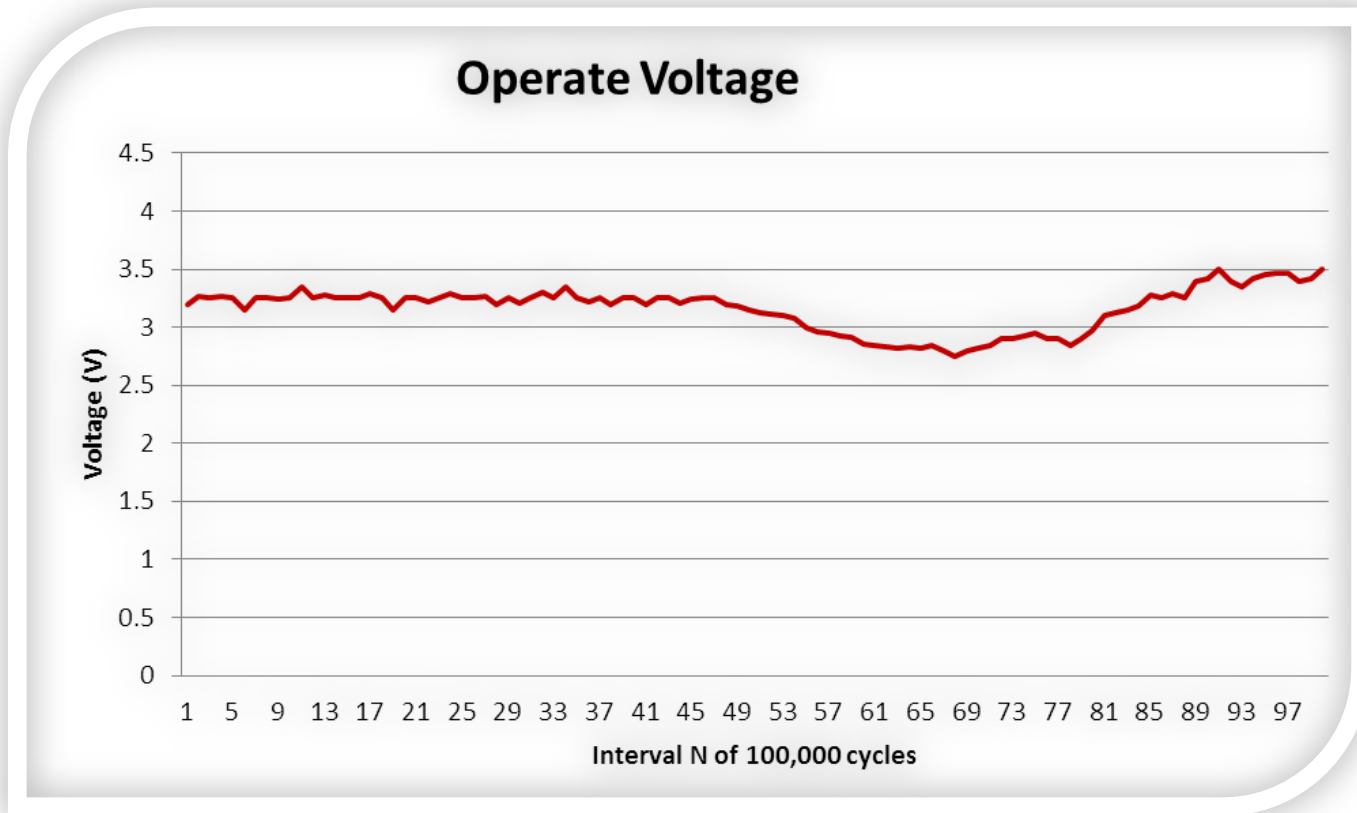
Operate/Release Voltage Test

- Voltage ramp test methodology
- Tested using 'Interval' method
- Life test paused for operate/release test

Monostable (non-latching):



Operate/Release Voltage Results

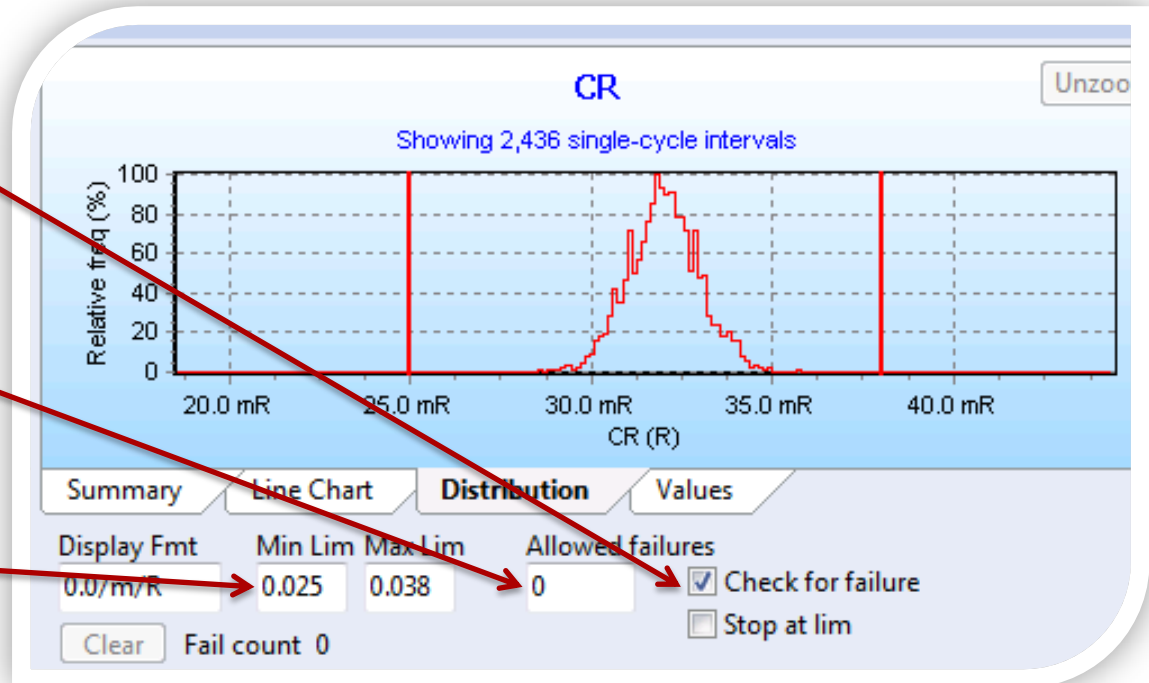


Failure Detection

Enable
Failure
Checking

Allowed
Failures

Min/Max
Failure
Limits



Conclusion

- Trend analysis using graphical representation
- Enhanced insight through contact timing & operate/release voltage
- Modular set of hardware resources

